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APPLICATION NO	. І	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/625,904		07/24/2003	Ritsuko Kawasaki	0756-7181	1203	
31780	7590	04/12/2005		EXAM	EXAMINER	
ERIC RO	BINSON		SEFER, A	SEFER, AHMED N		
PMB 955 21010 SOUTHBANK ST.				ART UNIT	PAPER NUMBER	
		VA 20165	2826			
				DATE MAILED: 04/12/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		Application No.	Applicant(s)			
		10/625,904	KAWASAKI ET AL.			
	omee negative can be a second	Examiner	Art Unit			
	The MAILING DATE of this communication app	A. Sefer	2826			
Period fo			ion copenzence dual cos			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status	•					
1)⊠	Responsive to communication(s) filed on 23 D	ecember 2004.				
2a)⊠	This action is FINAL . 2b)☐ This	action is non-final.				
3)[Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	Disposition of Claims					
4)⊠	Claim(s) 1-4 and 11-14 is/are pending in the a	pplication.				
• *	4a) Of the above claim(s) is/are withdrawn from consideration.					
5)[Claim(s) is/are allowed.					
6)⊠	Claim(s) <u>1-4 and 11-14</u> is/are rejected. Claim(s) is/are objected to.					
7)						
8)□	Claim(s) are subject to restriction and/o	r election requirement.				
Applicat	ion Papers					
9)[The specification is objected to by the Examine	r.				
10)	The drawing(s) filed on is/are: a) ☐ acc	epted or b) objected to by the I	Examiner.			
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)	The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.			
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachmen	t(s)					
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
	e of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	ate atent Application (PTO-152)			
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 7/2003.	6) Other:	atom reprincation (i 10-102)			

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DETAILED ACTION

Response to Amendment

1. The amendment filed 12/23/04 has been entered. Claims 5-10 have been cancelled and new claims 11-14 have been added.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

 (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1 and 3 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamazaki et al. ("Yamazaki") JP 11-4001.

Yamazaki discloses in figs. 1-2 a semiconductor device comprising: a light-transmitting substrate 101; a base film 106 having a projection, the film being formed over one surface of the light-transmitting substrate; and an island-like semiconductor layer 107 having a crystal structure covering the projection and extending over a pair of edges of the projection.

4. Claims 2 and 4 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamazaki.

Yamazaki discloses in figs. 1-2 a semiconductor device comprising: a light-transmitting substrate 101 and a thin film transistor over the light-transmitting substrate, wherein a base film 106 having a projection, the film being formed over one surface of the light-transmitting substrate; and an island-like semiconductor layer 107 comprising a channel formation region, at

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least a part of the channel formation region of the thin film transistor being provided over the projection and the island-like semiconductor layer covers the projection and extends over a pair of edges of the projection.

As for claims 3 and 4, the specification contains no disclosure of either the critical nature of the claimed arrangement or any unexpected results arising therefrom. Where patentability is said to be based upon particular chosen dimensions or upon another variable recited in a claim, the applicant must show that the chosen dimensions are critical. In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

5. Claim 11 and 13 are rejected under 35 U.S.C. 102(e) as being anticipated by Takeichi et al. ("Takeichi") USPN 6,258,723.

Takeichi discloses in fig. 5 a semiconductor device comprising a light-transmitting substrate 20, a base film 22 having a region of a first thickness (under channel formation region) and a region of a second thickness (not under channel formation region) smaller than the first thickness, the film being formed over one surface of the light- transmitting substrate, and the region of the first thickness having an area smaller than the region of the second thickness; and an island-like semiconductor layer 23 having a crystal structure over the base film, the layer being formed over the region of the first thickness and the region of the second thickness.

6. Claim 12 and 14 are rejected under 35 U.S.C. 102(e) as being anticipated by Takeichi.

Takeichi discloses in fig. 5 a semiconductor device comprising: a light-transmitting substrate 20 and a thin film transistor over the light-transmitting substrate, wherein a base film 22 having a region of a first thickness (under channel formation region) and a region of a second thickness (not under channel formation region) smaller than the first thickness being provided

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over one surface of the light-transmitting substrate, the region of the first thickness has an area smaller than the region of the second thickness, at least a pad of a channel formation region of the thin film transistor being provided over the region of the first thickness, source and drain regions of the thin film transistor are provided over the projection and cover a pair of edges of the projection, and the island-like semiconductor.

As for the recitation that the island-like semiconductor layer is capable of being irradiated with light, it refers to an operational limitation and any such limitation must distinguish from the prior art in terms of structure rather than function, In re Schreiber, 128 F.3d 1473, 1477-78, 44 USPQ2d 1429, 1431-32 (Fed. Cir. 1997), See also In re Swinehart, 439 F.2d210, 212-13, 169 USPQ 226, 228-29 (CCPA 1971; In re Danly, 263, F.2d 844, 847, 120 USPQ 528, 531 (CCPA 1959).

As for claims 13 and 14, the specification contains no disclosure of either the critical nature of the claimed arrangement or any unexpected results arising therefrom. Where patentability is said to be based upon particular chosen dimensions or upon another variable recited in a claim, the applicant must show that the chosen dimensions are critical. In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A. Sefer whose telephone number is (571) 272-1921.

If attempts to reach the examiner by telephone are unsuccessful, the examiner in J. FLYNN SUPERVISORY PATENT EXAMINE Supervisor, Nathan Flynn can be reached on (571) 272-1915.

TECHNOLOGY CENTER 2800

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ANS March 7, 2005